Search Notes

Application/Control No.	Applicant(s)/Patent under Reexamination	
10/681,860	CROSLAND ET AL.	
Examiner	**Art Unit**	
Christopher E. Lee	2111	

SEARCHED			
Class	Subclass	Date	Examiner
710	35 62 65 305 310	12/2/2005	CEL
709	253	12/2/2005	CEL
713	1	12/2/2005	CEL
710	10	4/7/2006	CEL
257	530	9/9/2006	CEL
711	167	5/2/2007	CEL
714	3 56	5/2/2007	CEL
327	144	5/2/2007	CEL
375	354	5/2/2007	CEL
710	58 105	12/19/2007	CEL

INTERFERENCE SEARCHED			
Class	Subclass	Date	Examiner

SEARCH NOTES (INCLUDING SEARCH STRATEGY)		
	DATE	EXMR
EAST (USPT, JPO, EPO, IBM_TDB) Bus Reset, Bridge Reset, Asynchronous Bus Bridging See Search History printout.	12/2/2005	CEL
EAST (USPT, JPO, EPO, IBM_TDB) circuit implementation in programmable logic and hard logic See Search History printout.	4/7/2006	CEL
EAST (USPT, JPO, EPO, IBM_TDB) programmable logic devices for bridging function	9/9/2006	CEL
EAST (USPT, JPO, EPO) programmable logic devices for bridging function; two clock domain reset; See search history ptintout.	5/2/2007	CEL
EAST (USPT, JPO, EPO) bridging device for two clock domains See search history ptintout.	12/19/2007	CEL